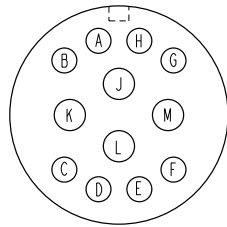


**Table 2-4 2105 Port Connector Functions<sup>a</sup>**

| Pin          | Wire Color | Functionality                    | Default Configuration          | Additional Information                      |
|--------------|------------|----------------------------------|--------------------------------|---|
| A - SDI-12A  | Brown      | SDI-12 Data                      | SDI-12 Data Master             | Transmits/Receives 0 to 5 VDC, 1200bps      |
| B - XMT-IO1B | Red        | I/O + Serial Out                 | TTL Serial Transmit            | Transmits 0 to 5 VDC, default 9600bps       |
| C - RCV-IO2C | Orange     | I/O + Serial In                  | TTL Serial Receive             | Receives $\pm 4$ to 26 VDC, default 9600bps |
| D - V5D-IO3D | Yellow     | I/O + Pulser                     | Flow Pulse Output <sup>b</sup> | Transmits 65-75 ms, 5 VDC pulses            |
| E - A2D-IO4E | Green      | I/O, Analog Voltage <sup>c</sup> | Sampler Inhibit Output         | Transmits sustained, 0 VDC for Inhibit      |
| F - A2D-IO5F | Blue       | I/O, Analog Voltage              | Sampler Event Mark Input       | Receives 3 second, 4 to 26 VDC pulses       |
| G - CTR-IO6G | Violet     | I/O, Analog Voltage              | Rain Gauge Tip Input           | Receives 48 ms, 0 VDC Rain pulses           |
| H - CTR-IO7H | Gray       | I/O + Counter                    | Sampler Bottle Number Input    | Receives 48 ms, 4 to 26 VDC pulses          |
| J - DGND-J   | Black      | Digital Ground                   | Digital Ground                 | 0 VDC, 100 mA typical                       |
| K - VBAT-K   | Tan        | Battery Voltage                  | Battery Voltage                | 7-26 VDC, 1A maximum                        |
| L - PGND-L   | White      | Power Ground                     | Power Ground                   | 0 VDC, 3A maximum                           |
| M - PSRC-M   | Pink       | Power Source                     | Power Source (switched)        | 9-12 VDC, 0.5A maximum                      |



- a. All voltage levels are positive default and referenced to common ground on pins J and L.  
b. All pulse signals are considered 50% duty cycle.  
c. See user manual for additional information on alternate configurations.